



#14
MDJ
6-6-03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Richard C. Foss, Peter B. Gillingham, Robert Harland,
Masami Mitsuhashi and Atsushi Wada

Application No.: 10/032,431 Group: 2653

Filed: December 21, 2001 Examiner: Dinh, Tan X.

Confirmation No.: 8755

For: DYNAMIC RANDOM ACCESS MEMORY USING IMPERFECT
ISOLATING TRANSISTORS

CERTIFICATE OF MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450	
on <u>5/27/03</u>	<u>Jennifer A. Tardiff</u>
Date	Signature
<u>Jennifer A. Tardiff</u>	
Typed or printed name of person signing certificate	

RECEIVED

JUN 04 2003

Technology Center 2600

REPLY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This Reply is being filed in response to the Office Action mailed from the U.S. Patent and Trademark Office on February 24, 2003 in the above-identified application. Reconsideration is requested.

An extension of time to respond to the Office Action is respectfully requested. A Petition for Extension of Time and the appropriate fee are being filed concurrently with this Reply.